

***Remarks***

Responsive to the Notice of Non-Compliant Amendment mailed February 3, 2004, Applicants have corrected the section of the Amendment deemed non-compliant (i.e., "Amendment to the Specification (Abstract)") by resubmitting that section in its entirety with an amended abstract marked up to show changes made relative to the immediate prior version. Additionally, the amended abstract is also submitted on a separate sheet without marked-up changes (i.e., in "clean form") pursuant to the Office Action dated July 17, 2003 (see, Paragraph 8).

By the foregoing amendment, Applicants believe the application is now in condition for allowance and respectfully request reconsideration.

If the Examiner believes, for any reason, that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at the number provided.

Respectfully submitted,

MIPS Technologies, Inc.

Date: April 2, 2004



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Attachment: Appendix: Abstract (in clean form)

**APPENDIX: Abstract (in clean form)**

Systems and methods that allow for performing a single transaction that both instructs a device to perform an operation and return the resulting data to a processor without the processor having to send a separate request for the result. In accordance with the systems and methods, a bus controller generates a system bus operation that sends (to the device) a thread identifier and a data request formulated in one thread by a processor that context switches to a second thread.